

MP01 Series

User's Manual

NO. G03-MP01-F

Revision: 5.0

Release date: December 8, 2023

Trademark:

* Specifications and Information contained in this documentation are furnished for information use only, and are subject to change at any time without notice, and should not be construed as a commitment by manufacturer.

Environmental Protection Announcement

Do not dispose this electronic device into the trash while discarding. To minimize pollution and ensure environment protection of mother earth, please recycle.



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Environmental Safety Instruction

- Avoid the dusty, humidity and temperature extremes. Do not place the product in any area where it may become wet.
- 0 to 60 centigrade is the suitable temperature. (The figure comes from the request of the main chipset)
- Generally speaking, dramatic changes in temperature may lead to contact malfunction and crackles due to constant thermal expansion and contraction from the 'welding spots' that connect components and PCB. Computer should go through an adaptive phase before it boots when it is moved from a cold environment to a warmer one to avoid condensation phenomenon. These water drops attached on PCB or the surface of the components can bring about phenomena as minor as computer instability resulted from corrosion and oxidation from components and PCB or as major as short circuit that can burn the components. Suggest starting the computer until the temperature goes up.
- The increasing temperature of the capacitor may decrease the life of computer. Using the close case may decrease the life of other device because the higher temperature in the inner of the case.
- Attention to the heat sink when you over-clocking. The higher temperature may decrease the life of the device and burned the capacitor.

USER'S NOTICE

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Manual Revision Information

| Reversion | Revision History | Date |
|-----------|------------------|------------------|
| 5.0 | Fifth Edition | December 8, 2023 |

Item Checklist

Motherboard

Chapter 1

Introduction of the Motherboard

1-1 Feature of Motherboard

- Onboard Intel® Elkhart Lake Series Processor, with low power consumption and high performance
- Support 1* DDR4 3200MHz SO-DIMM, up to 32GB
- 1* HDMI port & 1* eDP, Support Dual Displays
- 1* RS232/422/485 COM, 2* USB 3.1(Gen.2) Type-A, 2* USB 2.0, 1* USB 3.1(Gen.2) Type-C
- Onboard 1* M.2 M-key (2242, SATA/PCIe Gen.3 x2 interface) support NVME
- Onboard 1* M.2 E-key (2230, USB2.0/PCIe Gen.3 x1 interface)
- Support 2* Realtek RTL8119I 1.0Gbps LAN port
- Onboard optional 32GB / 64GB eMMC (by order)
- Compliance with ErP standard
- Support Watchdog function
- Solution for IoT, Machine Control & Intelligent Home

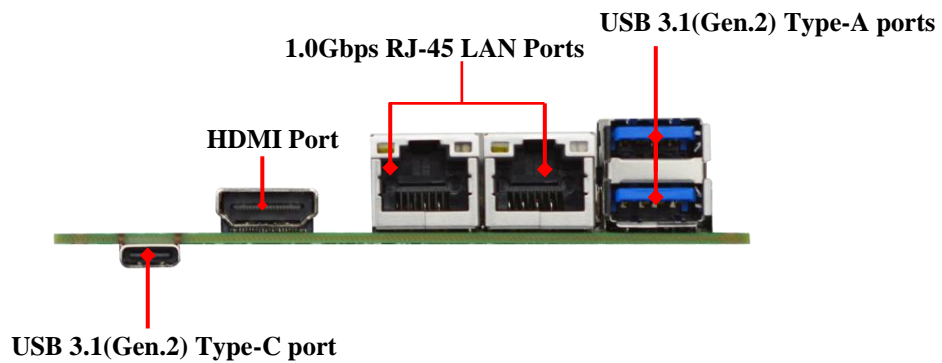
1-2 Specification

| Spec | Description |
|----------------|--|
| Design | <ul style="list-style-type: none"> ● 8-layer 2.5" SBC, Pico-size form factor; ● PCB size: 10.1cm x 7.2cm |
| Embedded CPU | <ul style="list-style-type: none"> ● Intel® Elkhart Lake series SoC CPU <p><i>* Note: for detailed CPU support information please visit our website</i></p> |
| Memory | <ul style="list-style-type: none"> ● 1* DDR4 SO-DIMM slot to support DDR4 3200MHz SO-DIMM up to 32GB (Memory frequency depending on CPU support) |
| Expansion Slot | <ul style="list-style-type: none"> ● M2E: 1* M.2 E-key (2230, USB2.0/PCIe Gen.3 x1 interface) <p><i>*Note: M2E slot maximum current limit is 2A while using 3.3V</i></p> |
| Storage | <ul style="list-style-type: none"> ● M2M1: 1* M.2 M-key (2242, SATA/PCIe Gen.3 x2 interface) support NVME <p><i>*Note: M2M1 slot maximum current limit is 2A while using 3.3V</i></p> <ul style="list-style-type: none"> ● Onboard optional 32GB / 64GB eMMC (by order) <p><i>*Note: Onboard eMMC capacity depends on the actual model purchased as technical specifications may update, without prior notice.</i></p> |
| LAN Chip | <ul style="list-style-type: none"> ● Integrated with 2* Realtek RTL8119I Gigabit LAN chip ● Support Fast Ethernet LAN function of providing 10/100/1000Mbps Ethernet data transfer rate |
| Audio Chip | <ul style="list-style-type: none"> ● Realtek HD audio chip |
| BIOS | <ul style="list-style-type: none"> ● AMI Flash ROM |
| Rear Panel I/O | <ul style="list-style-type: none"> ● 1* HDMI port ● 2* 1.0Gbps RJ-45 LAN port ● 2* USB 3.1(Gen.2) Type-A ports ● 1* USB 3.1(Gen.2) Type-C port (Backside) |
| Internal I/O | <ul style="list-style-type: none"> ● 1* 12V DC-in header ● 1* LAN activity LED header ● 1* ME Flash Override Select (JME) ● 1* GPIO port header ● 1* RS232/422/485 serial port (COM1) ● 1* eDP1 header ● 1* JBUZZ header |

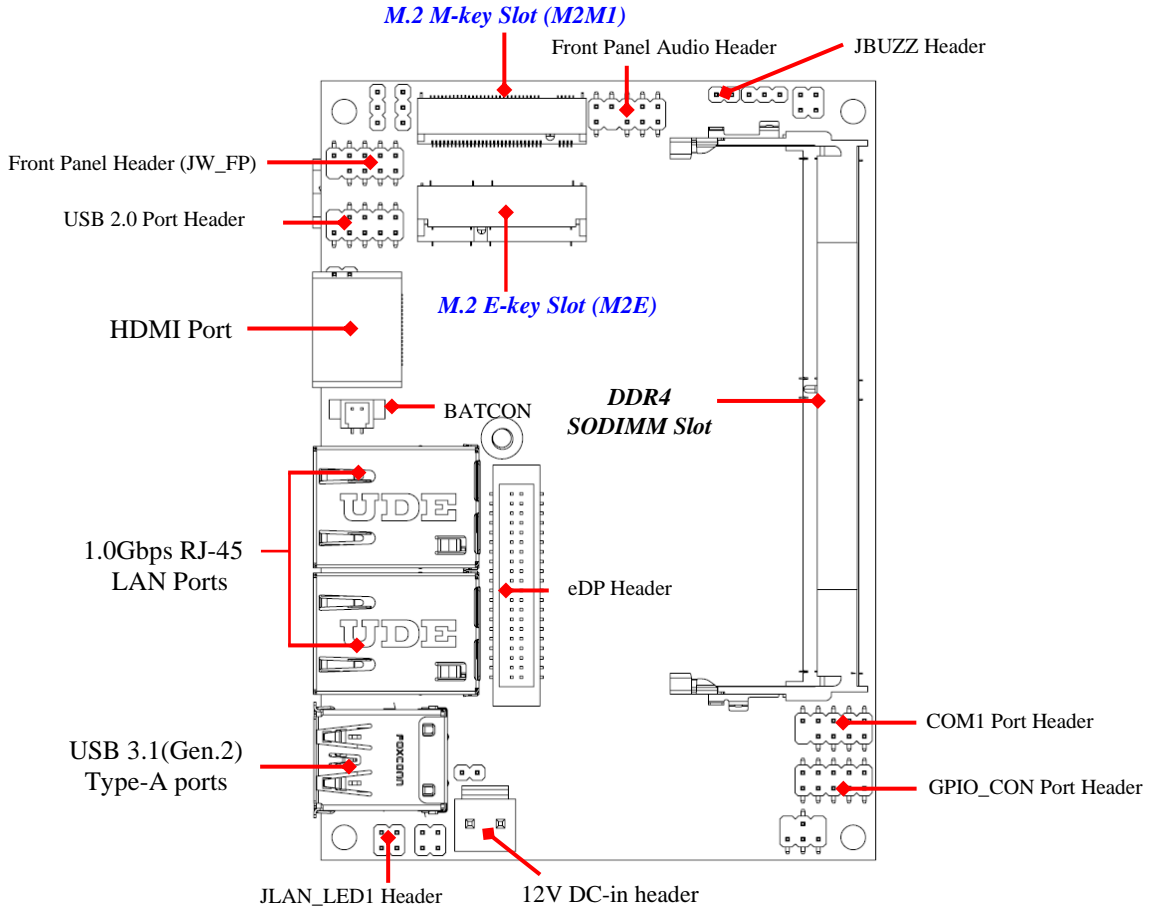
| | |
|--|---|
| | <ul style="list-style-type: none">● 1* Front panel audio header● 1* Front panel header (JW_FP)● 2* USB 2.0 port header |
|--|---|

1-3 Layout Diagram

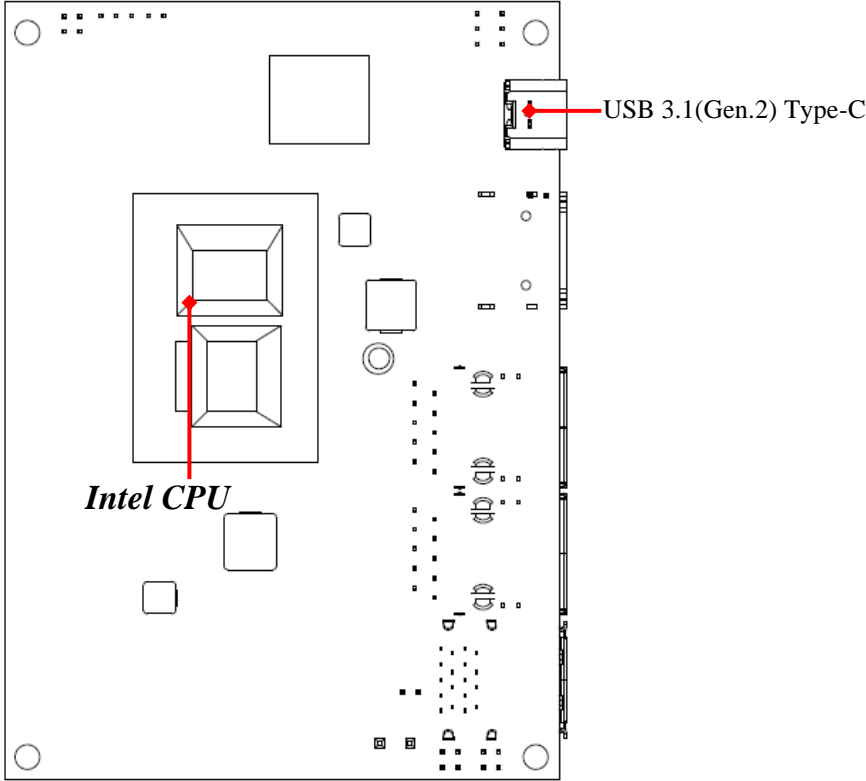
IO Panel Diagram:



Internal Diagram-Front Side:

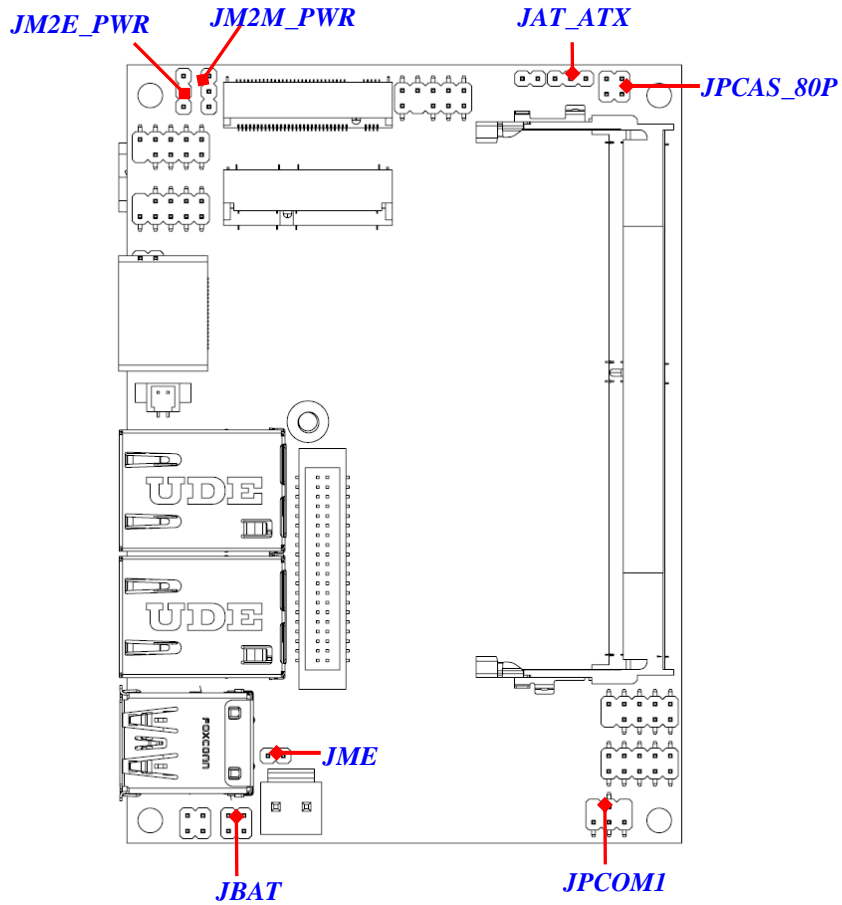


Internal Diagram-Back Side:



***Note:** eMMC is only optional to customized models. Please refer to the product you purchase for actual specifications. (Backside)

Motherboard Jumper Positions:



Jumper

| Jumper | Name | Description | Pitch |
|-----------|---|-------------|-------|
| JM2E_PWR | M2E Slot VCC Select | 3-Pin Block | 2.0mm |
| JM2M_PWR | M2M1 Slot VCC Select | 3-Pin Block | 2.0mm |
| JAT_ATX | ATX Mode/AT Mode Select | 3-Pin Block | 2.0mm |
| JPCOM1 | COM1 Header Pin9 Function Select | 4-Pin Block | 2.0mm |
| JBAT | PIN (1-2): Clear ME REG PIN (3-4): Clear CMOS | 4-Pin Block | 2.0mm |
| JPCAS_80P | PIN (1-2): Case Open Message Display Function PIN (3-4): GPIO_CON 80 Port/GPIO Function Select | 4-Pin Block | 2.0mm |
| JME | ME Flash Override Select | 2-pin Block | 2.0mm |

Connectors

| Connector | Name |
|--------------------|---------------------------------------|
| HDMI | HDMI Port Connector |
| LAN1/2 | 1.0Gbps RJ-45 LAN Port Connectors |
| USB3 | USB 3.1(Gen.2) Type-A Port Connectors |
| USBC_B1 (Backside) | USB 3.1(Gen.2) Type-C Port |

Headers

| Header | Name | Description | Pitch |
|-----------|--|-------------|-------|
| JW_FP | Front Panel Header(PWR LED/ HD LED/Power Button /Reset) | 9-pin Block | 2.0mm |
| FP_USB1 | USB 2.0 Port Header | 9-pin Block | 2.0mm |
| JLAN_LED1 | PIN (1-2): LAN1 Activity LED Headers PIN (3-4): LAN2 Activity LED Headers | 4-pin Block | 2.0mm |

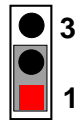
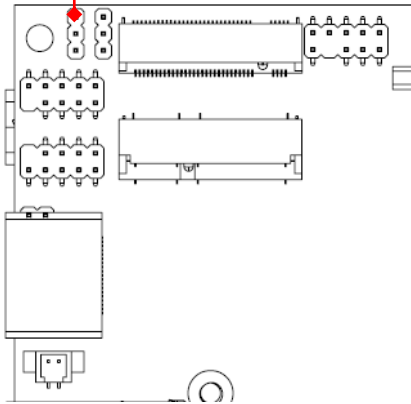
| | | | |
|----------|----------------------------------|--------------|--------|
| GPIO_CON | GPIO Port Header | 10-pin Block | 2.0mm |
| COM1 | RS232/485/422 Serial Port Header | 9-pin Block | 2.0mm |
| JBUZZ | Buzzer Header | 2-pin Block | 2.0mm |
| FP_AUDIO | Front Panel Audio Header | 9-pin Block | 2.0mm |
| DCIN2 | 12V DC-in Header | 2-pin Block | 3.96mm |
| EDP1 | eDP Port Header | 40-pin Block | 1.25mm |

Chapter 2 Hardware Installation

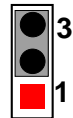
2-1 Jumper Settings

JM2E_PWR (3-pin): M2E Slot VCC Select *Pitch=2.0mm*

JM2E_PWR

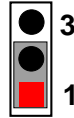
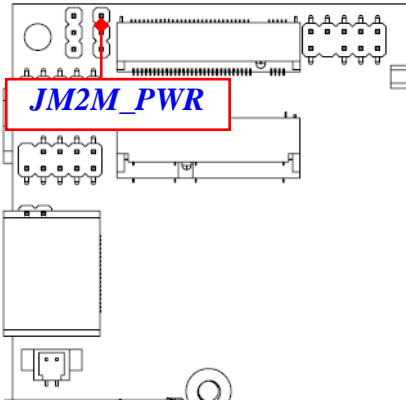


1-2 Closed: M2E Slot= VCC3;

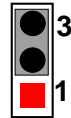


2-3 Closed: M2E Slot= 3VSB.

JM2M_PWR (3-pin): M2M1 Slot VCC Select *Pitch=2.0mm*

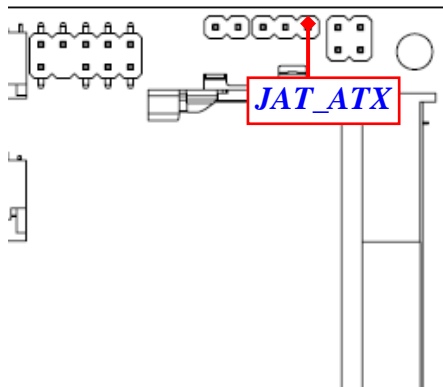


1-2 Closed: M2M1 Slot= VCC3;



2-3 Closed: M2M1 Slot= 3VSB.

JAT_ATX (3-pin): ATX Mode / AT Mode Select *Pitch=2.0mm*



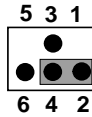
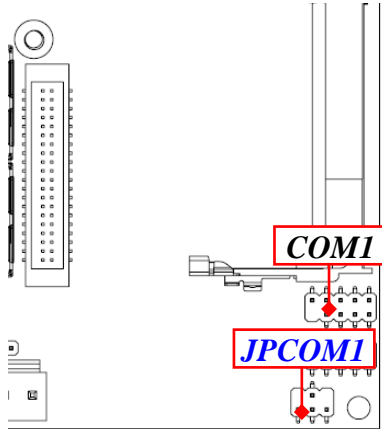
1-2 Closed: ATX Mode Selected



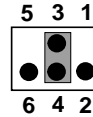
2-3 Closed: AT Mode Selected

***ATX Mode Selected:** Press power button to power on after power input ready;
AT Mode Selected: Directly power on as power input ready.

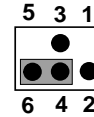
JPCOM1 (4-pin): COM1 Header Pin9 Function Select *Pitch=2.0mm*



2-4 Closed:
RI=RS232

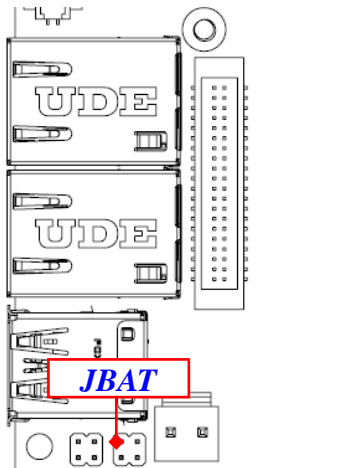


3-4 Closed:
RI=+5V;

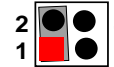


4-6 Closed:
RI=+12V.

Pin (1-2) of JBAT (4-pin): Clear ME REG *Pitch=2.0mm*

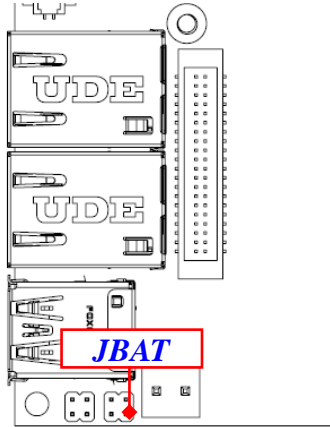


1-2 Open: Normal(Default);

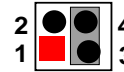


1-2 Closed:Clear ME REG

Pin (3-4) of JBAT (4-pin): Clear CMOS *Pitch=2.0mm*

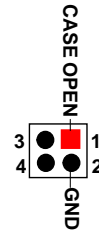
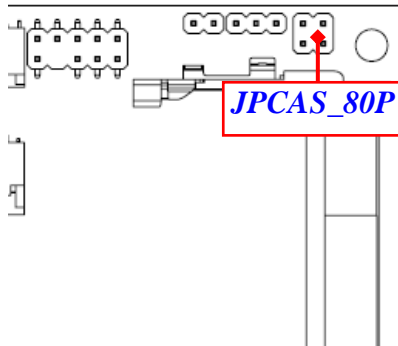


3-4 Open: Normal(Default);



3-4 Closed:Clear CMOS

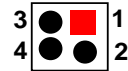
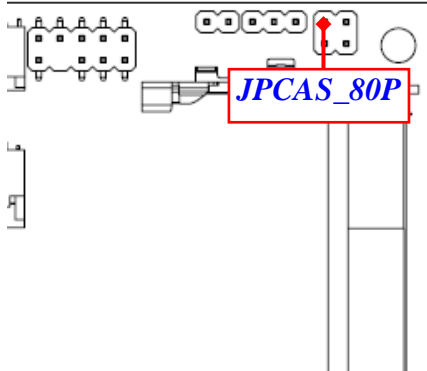
**PIN (1-2) of JPCAS_80P (4-pin) : Case Open Message Display Function
*Pitch=2.0mm***



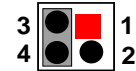
JPCAS_80P (1-2) = CASE OPEN

JPCAS_80P Pin (1-2) short: When Case open function pin short to GND, the Case open function was detected. When used, needs to enter BIOS and enable 'Case Open Detect' function. In this case if your case is removed, next time when you restart your computer, a message will be displayed on screen to inform you of this.

PIN (3-4) of JPCAS_80P (4-pin) : GPIO_CON 80 Port/GPIO Function Select
Pitch=2.0mm

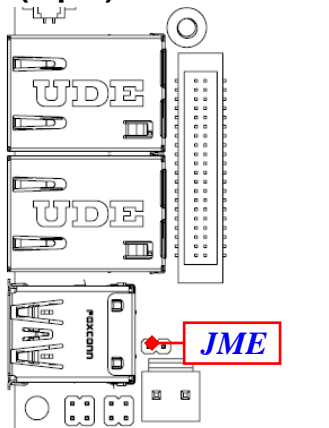


3-4 Open: GPIO_CON= GPIO;

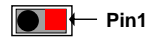


3-4 Closed: GPIO_CON=80 PORT.

JME (2-pin): ME Flash Override Select **Pitch=2.0mm**



1-2 Open: Normal (Default);

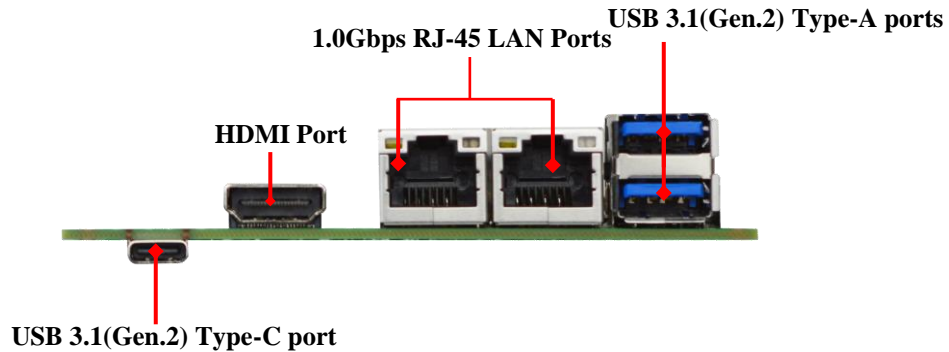






1-2 Closed: ME Flash Override

2-2 Connectors and Headers

2-2-1 Connectors

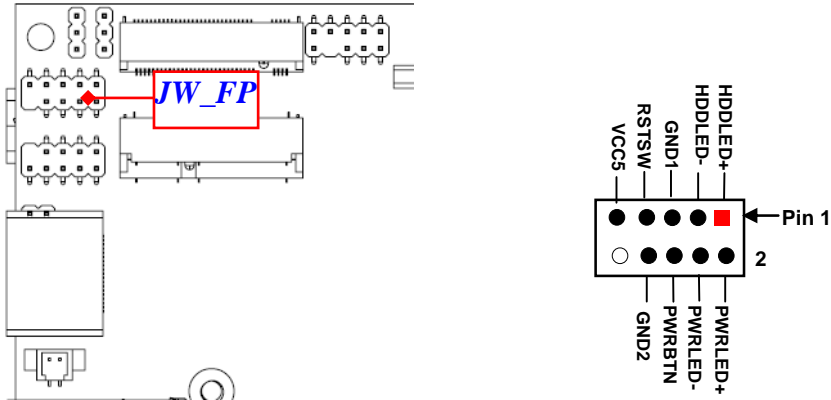
(1) Rear Panel Connectors



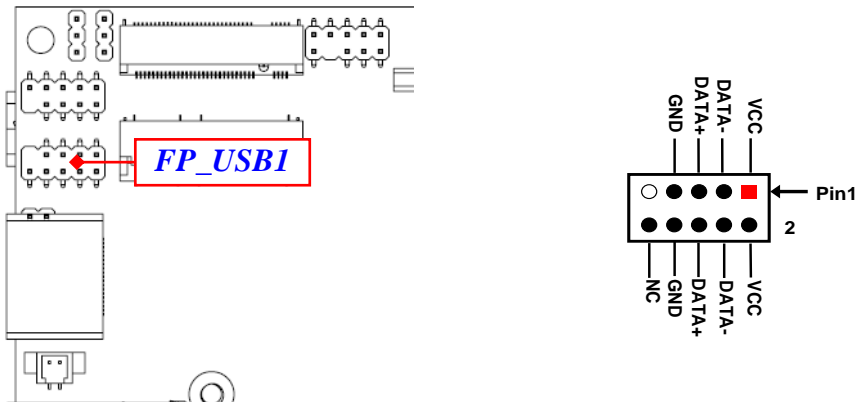
| Icon | Name | Function |
|---|----------------------------|--|
|  | USB 3.1(Gen.2) Type-C port | USB 3.1(Gen.2) Type-C supports USB function only. (Backside) |
|  | HDMI Port | To connect display device that support HDMI specification. |
|  | RJ-45 LAN Port | This connector is standard 1.0Gbps RJ-45 LAN jack for Network connection. |
|  | USB 3.1(Gen.2) Type-A Port | To connect USB keyboard, mouse or other devices compatible with USB specification. USB 3.1(Gen.2) Type-A Port ports supports up to 5Gbps data transfer rate. |

2-2-2 Headers

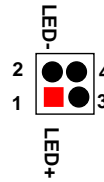
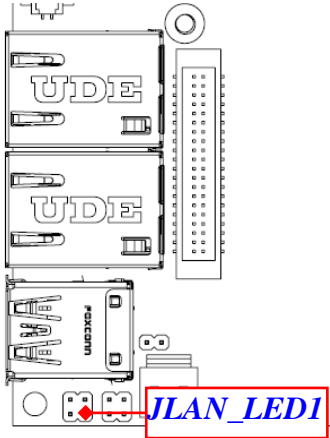
(1) JW_FP (9-pin): Front Panel Header *Pitch=2.0mm*



(2) FP_USB1 (9-pin): USB 2.0 Port Header *Pitch=2.0mm*

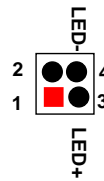
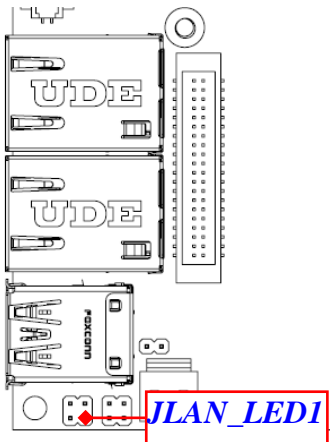


(3) JLAN_LED1 PIN (1-2): LAN1 Activity LED Headers *Pitch=2.0mm*



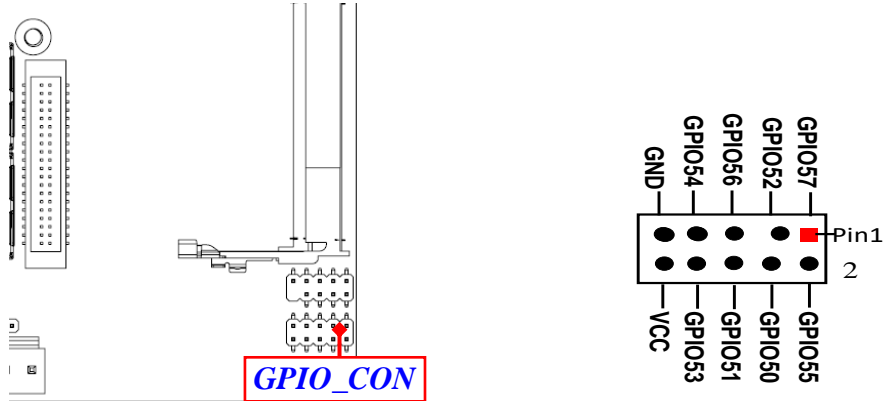
JLAN_LED(1-2)=FOR LAN1_LED

(4) JLAN_LED1 PIN (3-4): LAN2 Activity LED Headers *Pitch=2.0mm*



JLAN_LED(3-4)=FOR LAN2_LED

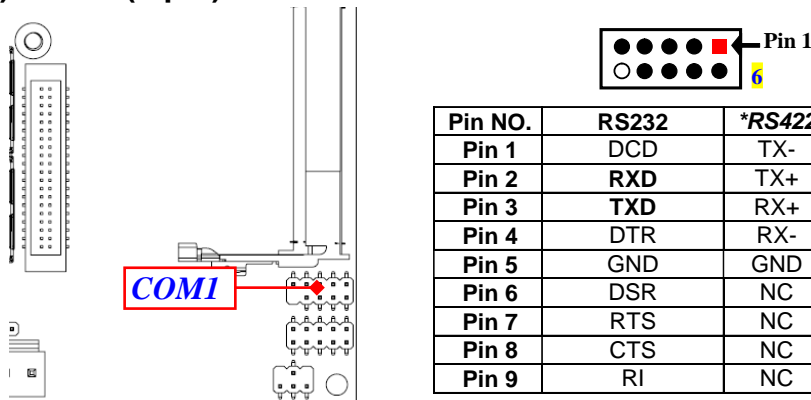
(5) GPIO_CON (10-pin): GPIO Port Header *Pitch=2.0mm*



**Note:* Please refer to Page-12 JPCAS_80P jumper setting for GPIO header GPIO Port or 80 Port function select:

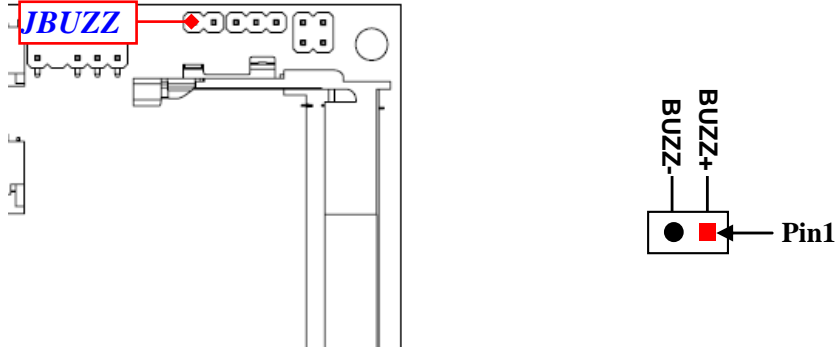
- **Pin 3&4 of JPCAS_80P Open:** For Normal 8-bit GPIO Function;
Pin 3&4 of JPCAS_80P Closed: For 80Port Function

(6) COM1 (9-pin): RS232/485/422 Serial Port Header *Pitch=2.0mm*



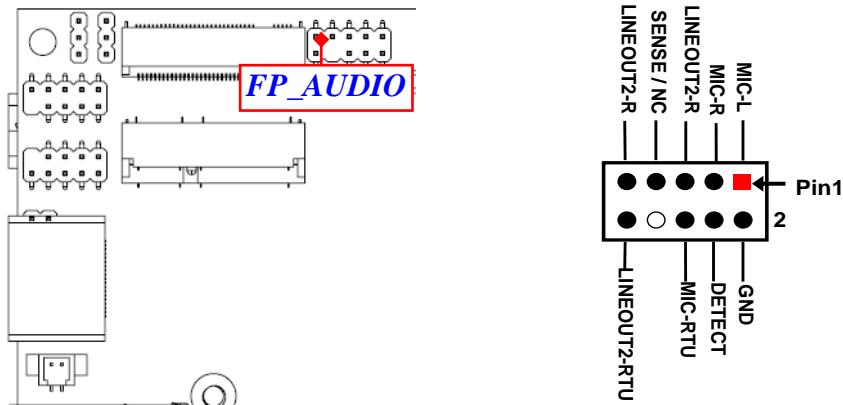
| Pin NO. | RS232 | *RS422 | *RS485 |
|---------|-------|--------|--------|
| Pin 1 | DCD | TX- | DATA- |
| Pin 2 | RXD | TX+ | DATA+ |
| Pin 3 | TXD | RX+ | NC |
| Pin 4 | DTR | RX- | NC |
| Pin 5 | GND | GND | GND |
| Pin 6 | DSR | NC | NC |
| Pin 7 | RTS | NC | NC |
| Pin 8 | CTS | NC | NC |
| Pin 9 | RI | NC | NC |

(7) JBUZZ (2-pin): Buzzer Header *Pitch=2.0mm*

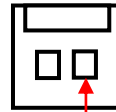
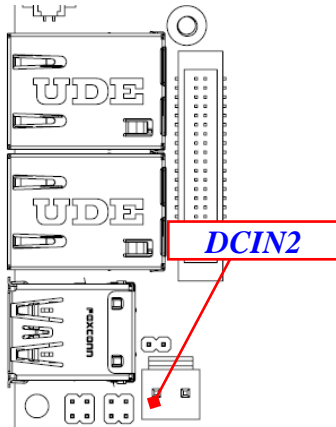


(8) FP_AUDIO (9-pin): Line-Out, MIC-In Header *Pitch=2.0mm*

This header connects to Front Panel Line-out, MIC-In connector with cable.



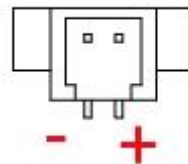
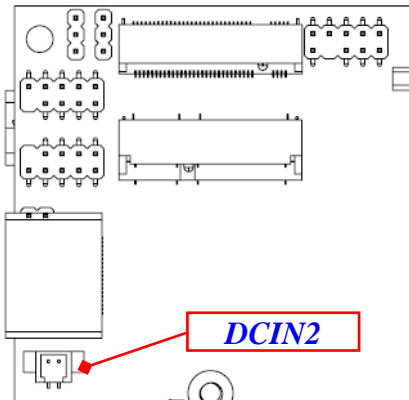
(9) DCIN2 (2-pin block): Internal 12V DC-in Header *Pitch=3.96mm*



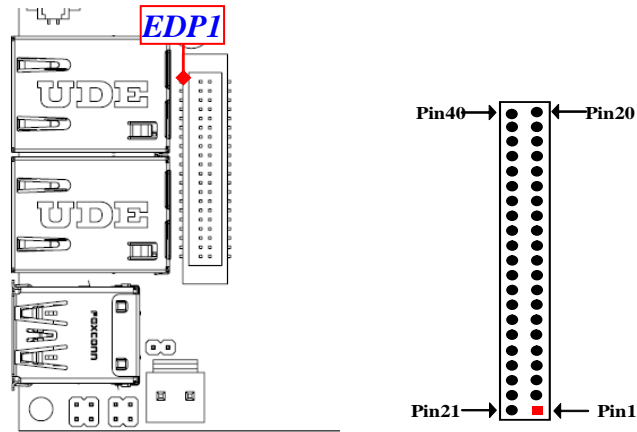
Pin1

| Pin No. | Definition |
|---------|------------|
| 1 | 12V DC-In |
| 2 | GND |

(10) BATCON (2-pin): Battery Connector



(11) EDP1 (40-pin): 4-Lane eDP Header *Pitch=1.25mm*



| Pin NO. | Pin Define | Pin NO. | Pin Define |
|---------|--------------|---------|----------------|
| Pin 40 | BL_PWR (12V) | Pin 20 | LCD_VCC (3.3V) |
| Pin 39 | BL_PWR (12V) | Pin 19 | LCD_VCC (3.3V) |
| Pin 38 | BL_PWR (12V) | Pin 18 | LCD_VCC (3.3V) |
| Pin 37 | BL_PWR (12V) | Pin 17 | GND |
| Pin 36 | BL_PWR (12V) | Pin 16 | AUX_CH_N |
| Pin 35 | NC | Pin 15 | AUX_CH_P |
| Pin 34 | NC | Pin 14 | GND |
| Pin 33 | BL_PWM_DIM | Pin 13 | Lane0_P |
| Pin 32 | BL_ENABLE | Pin 12 | Lane0_N |
| Pin 31 | GND | Pin 11 | GND |
| Pin 30 | GND | Pin 10 | Lane1_P |
| Pin 29 | GND | Pin 9 | Lane1_N |
| Pin 28 | GND | Pin 8 | GND |
| Pin 27 | HPD | Pin 7 | Lane2_P |
| Pin 26 | GND | Pin 6 | Lane2_N |
| Pin 25 | GND | Pin 5 | GND |
| Pin 24 | GND | Pin 4 | Lane3_P |
| Pin 23 | GND | Pin 3 | Lane3_N |
| Pin 22 | NC | Pin 2 | GND |
| Pin 21 | NC | Pin 1 | NC |

2-2-3 Maximum Voltage & Current Limit

Below is a list of maximum voltage & Current Limit specification for motherboard interface (including but not limited to slots, connectors, wafers and headers) for setup reference:

| Location | Function | Pin Define | Working Voltage | Current Support |
|------------------|-----------------------------|----------------------------|-----------------|-----------------|
| USB3 | USB 3.1(Gen.2) Type-A | | 5V | 1.5A |
| USBC_B1 | USB 3.1(Gen.2) Type-C | | 5V | 1.5A |
| DCIN2 | DC-in Header | | 12V | 5A |
| FP_USB1 | USB 2.0 Port | | 5V | 0.5A |
| GPIO_CON | GPIO Port | | 5V | 1A |
| JW_FP | Front Panel Header | | 5V | 1A |
| EDP1 | eDP Port | eDP LCD VCC (Pin18-20) | 3.3V | 2A |
| | | eDP BKLT PWR (Pin36-40) | 12V | 2A |
| JLAN_LED1 | LAN Activity LED | | 3.3V | 0.5A |
| JM2E_PWR | M2E Slot VCC Select | | 3.3V | 2A |
| JM2M_PWR | M2M1 Slot VCC Select | | 3.3V | 2A |
| JPCOM1 | COM1 Header Function Select | Pin9 | 5V/12V | 0.5A |

Chapter 3

Introducing BIOS

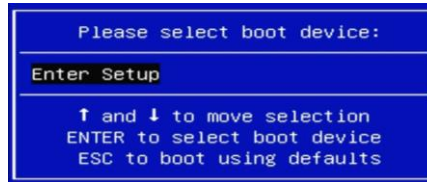
Notice! The BIOS options in this manual are for reference only. Different configurations may lead to difference in BIOS screen and BIOS screens in manuals are usually the first BIOS version when the board is released and may be different from your purchased motherboard. Users are welcome to download the latest BIOS version form our official website.

The BIOS is a program located on a Flash Memory on the motherboard. This program is a bridge between motherboard and operating system. When you start the computer, the BIOS program will gain control. The BIOS first operates an auto-diagnostic test called POST (power on self test) for all the necessary hardware, it detects the entire hardware device and configures the parameters of the hardware synchronization. Only when these tasks are completed done it gives up control of the computer to operating system (OS). Since the BIOS is the only channel for hardware and software to communicate, it is the key factor for system stability, and in ensuring that your system performance as its best.

3-1 Entering Setup

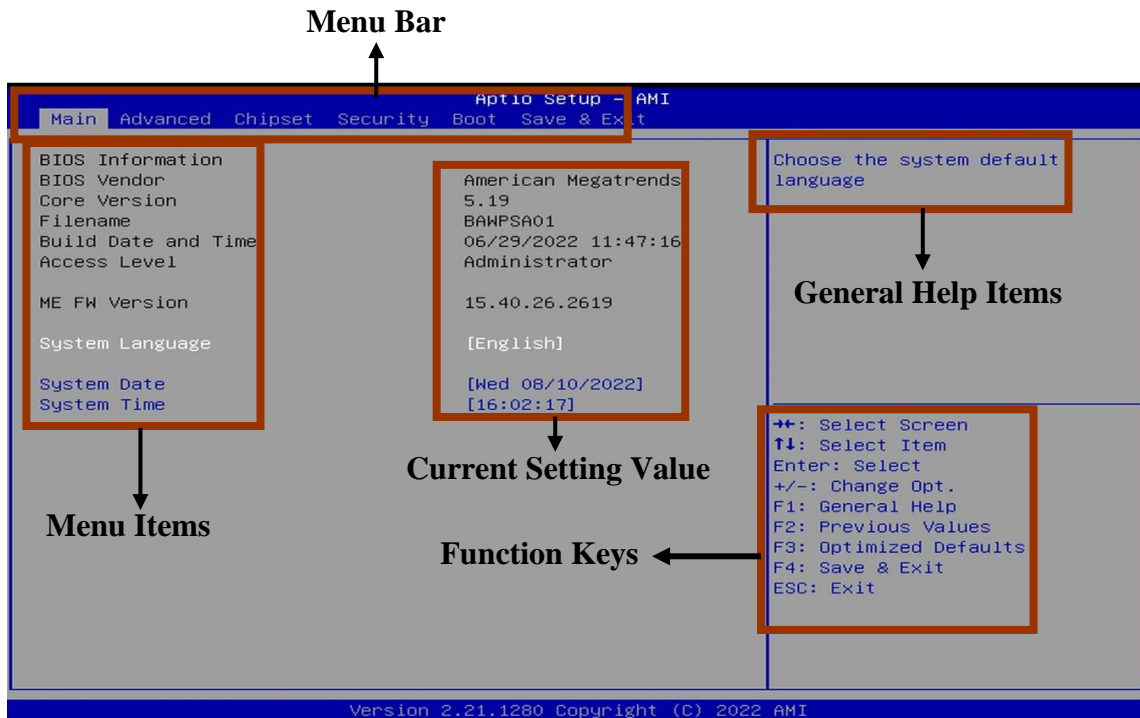
Power on the computer and by pressing immediately allows you to enter Setup. If the message disappears before your respond and you still wish to enter Setup, restart the system to try again by turning it OFF then ON or pressing the “RESET” button on the system case. You may also restart by simultaneously pressing <Ctrl>, <Alt> and <Delete> keys. If you do not press the keys at the correct time and the system does not boot, an error message will be displayed and you will again be asked to

Press to enter Setup/ Press <F7> to enter Popup Menu.



3-2 BIOS Menu Screen

The following diagram show a general BIOS menu screen:



3-3 Function Keys

In the above BIOS Setup main menu of, you can see several options. We will explain these options step by step in the following pages of this chapter, but let us first see a short description of the function keys you may use here:

- Press ←→ (left, right) to select screen;
- Press ↑↓ (up, down) to choose, in the main menu, the option you want to confirm or to modify.
- Press <Enter> to select.
- Press <+>/<-> keys when you want to modify the BIOS parameters for the active option.
- [F1]: General help.
- [F2]: Previous value.
- [F3]: Optimized defaults.
- [F4]: Save & Exit.
- Press <Esc> to quit the BIOS Setup.

3-4 Getting Help

Main Menu

The on-line description of the highlighted setup function is displayed at the top right corner the screen.

Status Page Setup Menu/Option Page Setup Menu

Press [F1] to pop up a small help window that describes the appropriate keys to use and the possible selections for the highlighted item. To exit the Help Window, press <Esc>.

3-5 Menu Bars

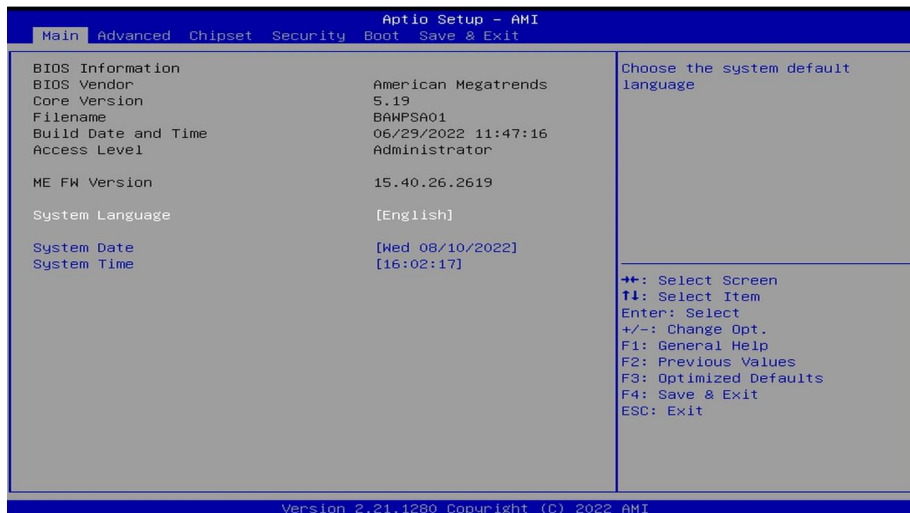
There are six menu bars on top of BIOS screen:

| | |
|------------------------|---|
| Main | To change system basic configuration |
| Advanced | To change system advanced configuration |
| Chipset | To change chipset configuration |
| Security | Password settings |
| Boot | To change boot settings |
| Save & Exit | Save setting, loading and exit options. |

User can press the right or left arrow key on the keyboard to switch from menu bar. The selected one is highlighted.

3-6 Main Menu

Main menu screen includes some basic system information. Highlight the item and then use the <+> or <-> and numerical keyboard keys to select the value you want in each item.



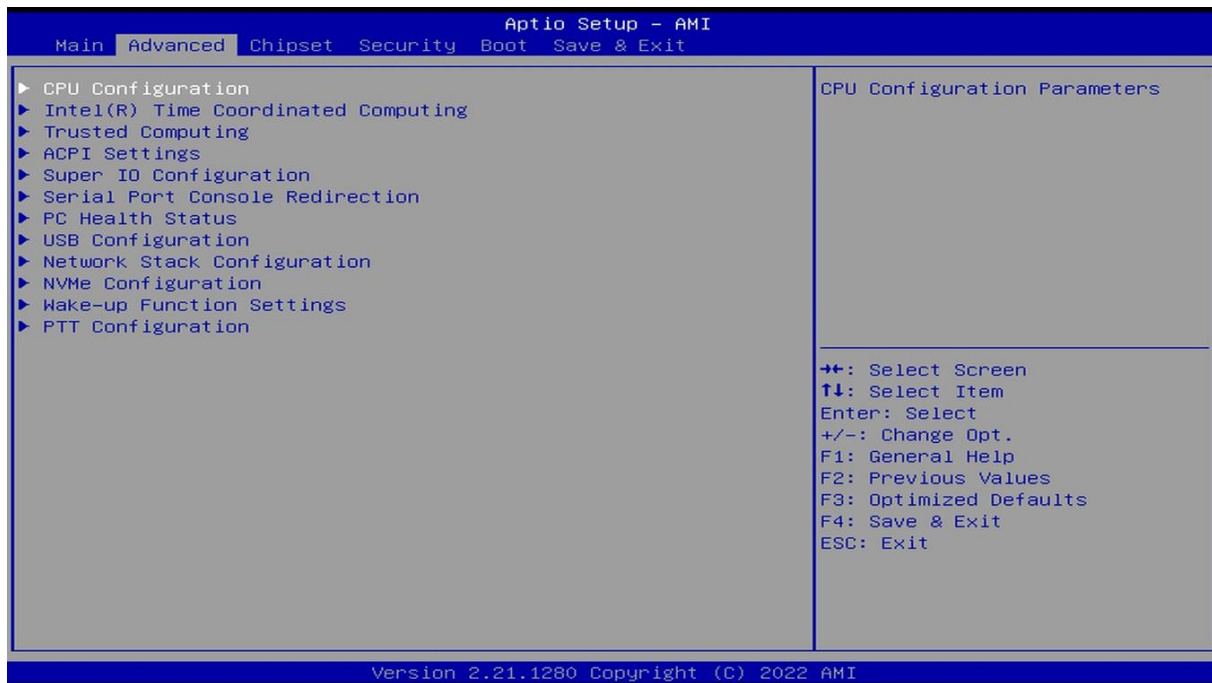
System Date

Set the date. Please use [Tab] to switch between date elements.

System Time

Set the time. Please use [Tab] to switch between time elements.

3-7 Advanced Menu



▶ CPU Configuration

Press [Enter] to make settings for the following sub-items.

Boot Performance Mode

Use this item to select the performance state that the BIOS will set starting from reset vector.

The optional settings: [Max Battery]; [Max Non-Turbo Performance] ; [Turbo Performance]

Intel(R) SpeedStep (tm)

Use this item to allows more than two frequency ranges to be supported.

The optional settings: [Disabled]; [Enabled].

When set as [Enabled], the following item shall appear:

Turbo Mode

Use this item to enable/disable processor Turbo Mode (requires EMTTM enabled too). AUTO means enabled.

C states

Use this item to enable/disable CPU Power Management. Allows CPU to go to C states when it's not 100% utilized.

The optional settings: [Disabled]; [Enabled].

When set as [Enabled], the following item shall appear:

Enhanced C-states

Use this item to enable/disable C1E. When enabled, CPU will switch to minimum speed when all cores enter C-State

The optional settings: [Disabled]; [Enabled].

Package C State Limit

Use this item to Maximum Package C State Limit Setting. CPU Default: Leaves to Factory default value.

Auto: Initializes to deepest available Package C State Limit.

The optional settings: [C0/C1]; [C2]; [C3]; [C6]; [C7]; [C7S]; [C8]; [C9]; [C10]; [CPU Default]; [Auto]

Power Limit 1 Override

Use this item to enable/disable Power Limit 1 override. If this option is disable, BIOS will program the default values for Power Limit 1 and Power Limit 1 Time Window.

The optional settings: [Disabled]; [Enabled].

Power Limit 1

Use this item to power Limit 1 in Milli Watts. BIOS will round to the nearest 1/8W

when programming. 0= no custom override. For 12.50W, enter 12500. Overclocking SKU: Value must be between Max and Min Power Limits (specified by PACKAGE_POWER_SKU_MSR). Other SKUs: This value must be between Min Power Limit and TDP Limit. If value is 0, BIOS will program TDP value.

Power Limit 1 Time Window

Use this item to power Limit 1 Time Window value in seconds. The value may vary from 0 to 128. 0=default value (28 sec for Mobile and 8 sec for desktop). Defines time Window which TDP value should be maintained.

The optional settings: [0]; [1]; [2]; [3]; [4]; [5]; [6]; [7]; [8]; [10]; [12]; [14]; [16]; [20]; [24]; [28]; [32]; [40]; [48]; [56]; [64]; [80]; [96]; [112]; [128]

Power Limit 2 Override

Use this item to enabled/disable power Limit 2 override. If this option is disabled, BIOS will program the default values for Power Limit 2.

The optional settings: [Disabled]; [Enabled].

When set as [Enabled], user can make further settings in the following items:

Power Limit 2

Use this item to power Limit 2 value in Milli watts. BIOS will round to the nearest 1/8W when programming. If the value is 0, BIOS will program this value as 1.25*TDP. For 12.50W, enter 12500. Processor applies control policies such that the package power does not exceed this limit.

▶ **Intel(R) Time Coordinated Computing**

Use this item to Intel(R) Time Coordinated Computing (Intel(R) TCC) options

Intel(R) TCC Mode

Use this item to enable or disable Intel(R) TCC mode. when enabled, this will modify system settings to improve real-time performance. The full list of settings and their current state are displayed below when Intel(R) TCC mode is enabled

The optional settings: [Disabled]; [Enabled].

When set as [Enabled], user can make further settings in the following items:

Intel(R) TCC Authentication

Use this item to enable/disable authentication of Intel(R) TCC configuration data.

The optional settings: [Disabled]; [Enabled].

When set **Intel(R) TCC Mode** as [Disabled], user can make further settings in the following items:

IO Fabric Low Latency

Use this item to Enable or Disable IO Fabric Low Latency. This will turn off some power management in the PCH IO fabrics. This option provides the most aggressive IO Fabric performance setting. S3 state is NOT supported.

The optional settings: [Disabled]; [Enabled].

GT CLOS

Use this item to Enable or Disable Graphics Technology(GT) Class of Service. Enable will reduce Gfx LLC allocation to minimize impact of Gfx workload on LLC

The optional settings: [Disabled]; [Enabled].

▶ **Trusted Computing**

Press [Enter] to enable or disable 'Security Device Support'

TPM20 Device Found

Security Device Support

Use this item to Enable or Disable BIOS support for security device. O.S. will not show Security Device. TCG EFI protocol and INT1A interface will not be available.

The optional settings: [Disabled]; [Enabled].

When set as **[Enabled]**, user can make further settings in the following items:

Active PCR Banks

Available PCR Banks

SHA-1 PCR Bank

Use this item to Enable or Disable SHA-1 PCR Bank

The optional settings: [Disabled]; [Enabled].

SHA256 PCR Bank

Use this item to Enable or Disable SHA256 PCR Bank

The optional settings: [Disabled]; [Enabled].

SHA384 PCR Bank

Use this item to Enable or Disable SHA384 PCR Bank

The optional settings: [Disabled]; [Enabled].

SM3_256 PCR Bank

Use this item to Enable or Disable SM3_256 PCR Bank

The optional settings: [Disabled]; [Enabled].

Pending Operation

Use this item to schedule an operation for the security device

NOTE: Your computer will reboot during restart in order to change state of security device

The optional settings: [None]; [TPM Clear].

▶ **ACPI Settings**

Press [Enter] to make settings for the following sub-item:

ACPI Sleep State

Use this item to select the highest ACPI sleep state the system will enter when the SUSPEND button is pressed.

The optional settings: [Suspend Disabled]; [S3(Suspend to RAM)].

▶ **Super I/O Configuration**

Press [Enter] to make settings for the following sub-item:

Super IO Configuration

▶ **Serial Port 1 Configuration**

Use this item to set parameters of serial port 1 (COMA)

Press [Enter] to make settings for the following sub-item:

Serial Port

Use this item to enable or disable serial port (COM)

Device Settings

When set as [**Enabled**], user can make further settings in the following items:

Change Settings

Use this item to select an optimal settings for super IO device

The optional settings: [Auto]; [IO= 3F8h; IRQ=4]; [IO= 2F8h; IRQ=3]; [IO= 3E8h;

IRQ=4]; [IO= 2E8h; IRQ=3]

Transmission Mode Select

The optional settings: [RS422]; [RS232]; [RS485]

Mode Speed Select

Use this item to RS232/RS422/RS485 Speed Select

The optional settings: [RS232/RS422/RS485= 250kps]; [RS232=1Mbps, RS422/RS485= 10Mbps]

ERP Support

Use this item to Energy-Related Products function. Disable ERP to active all wake-up functions.

The optional settings: [Disabled]; [Enabled].

Case Open Detect

Use this item to detect if case have ever been opened. Show message in POST.

The optional settings: [Disabled]; [Enabled].

When set as [Enabled], system will detect if COPEN has been short or not (*refer to JPCAS_80P jumper setting for Case Open Detection*); if Pin 1-2 is short, system will show Case Open Message during POST.

WatchDog Reset Timer

Use this item to support WDT reset function

The optional settings: [Disabled]; [Enabled].

When set as **[Enabled]**, user can make further settings in the following items:

WatchDog Reset Timer Value

User can select a value in the range of [10] to [255] seconds when 'WatchDog Reset Timer Unit' set as [Sec]; or in the range of [1] to [255] minutes when 'WatchDog Reset Timer Unit' set as [Min].

WatchDog Reset Timer Unit

The optional settings: [Sec.]; [Min.].

WatchDog Wake-up Timer

User this item to support WDT Wake-up

The optional settings: [Disabled]; [Enabled].

When set as **[Enabled]**, user can make further settings in the following items:

WatchDog Reset Timer Value

User can select a value in the range of [10] to [255] seconds when 'WatchDog Reset Timer Unit' set as [Sec]; or in the range of [1] to [255] minutes when 'WatchDog Reset Timer Unit' set as [Min].

WatchDog Reset Timer Unit

The optional settings: [Sec.]; [Min.].

ATX Power Emulate AT Power

This item support Emulate AT power function, MB power On/Off control by power supply. Use needs to select 'AT or ATX Mode' on MB jumper at first (refer to JAT_ATX jumper setting Pin 1&2 of for ATX Mode & Pin 2&3 of AT Mode Select)

▶ **Serial Port Console Redirection**

User this item to serial port console redirection

Press [Enter] to make settings for the following sub-item:

COM1

Console Redirection

User this item to Console Redirection Enable or Disable

The optional settings: [Disabled]; [Enabled].

When set as [**Enabled**], user can make further settings in the following items:

▶ **Console Redirection Settings**

User this item to the settings specify how the host computer and the remote computer (which the user is using) will exchange data. Both computers should have the same or compatible settings

Press [Enter] to make settings for the following sub-item:

Terminal Type

The optional settings: [VT100]; [VT100+]; [VT-UTF8]; [ANSI].

Emulation: [ANSI]: Extended ASCII char set; [VT100]: ASCII char set; [VT100+]:

Extends VT100 to support color, function keys, etc.; [VT-UTF8]: Uses UTF8 encoding to map Unicode chars onto 1 or more bytes.

Bits per second

Use this item to select serial port transmission speed. The speed must be

matched on the other side. Long or noisy lines may require lower speeds.

The optional settings: [9600]; [19200]; [38400]; [57600]; [115200].

Data Bits

Use this item to data bits

The optional settings: [7]; [8]

Parity

A parity bit can be sent with the data bits to detect some transmission errors.

The optional settings: [None]; [Even]; [Odd]; [Mark]; [Space].

[Even]: parity bit is 0 if the num of 1's in the data bits is even; [Odd]: parity bit is 0 if num of 1's in the data bits is odd; [Mark]: parity bit is always 1; [Space]: Parity bit is always 0; [Mark] and [Space] Parity do not allow for error detection.

Stop Bits

Stop bits indicate the end of a serial data packet. (A start bit indicates the beginning). The standard setting is 1 stop bit. Communication with slow devices may require more than 1 stop bit.

The optional settings: [1]; [2].

Flow Control

Flow control can prevent data loss from buffer overflow. When sending data, if the receiving buffers are full, a "stop" signal can be sent to stop the data flow. Once the buffers are empty, a "start" signal can be sent to re-start the flow. Hardware flow control uses two wires to send start/stop signals.

The optional settings: [None]; [Hardware RTS/CTS].

VT-UTF8 Combo key Support

Use this item to enable VT-UTF8 combination key support for ANSI/VT100 terminals.

The optional settings: [Disabled]; [Enabled].

Recorder Mode

Use this item to with this mode enabled only text will be sent. This is to capture Terminal data.

The optional settings: [Disabled]; [Enabled].

Resolution 100X31

Use this item to Enable or disable extended terminal resolution

The optional settings: [Disabled]; [Enabled].

Putty KeyPad

Use this item to select FunctionKey and KeyPad on Putty

The optional settings: [VT100]; [Linux]; [XTERMR6]; [SCO]; [ESCN]; [VT400].

Serial Port for Out-of-Band Management/

Windows Emergency Management Services (EMS)

Console Redirection EMS

The optional settings: [Disabled]; [Enabled]. When set as [Enabled], the following sub-items shall appear:

▶ Console Redirection Settings

The settings specify how the host computer and the remote computer (which the user is using) will exchange data. Both computers should have the same or compatible settings.

Press [Enter] to make settings for the following items:

Out-of-Band Mgmt Port

The optional setting is: [COM1].

Terminal Type EMS

The optional settings: [VT100]; [VT100+]; [VT-UTF8]; [ANSI].

[VT-UTF8] is the preferred terminal type for out-of-band management. The next best choice is [VT100+] and then [VT100]. See above, in Console Redirection Settings page, for more help with Terminal Type/Emulation.

Bits per second

Use this item to select serial port transmission speed. The speed must be matched on the other side. Long or noisy lines may require lower speeds.

The optional settings: [9600]; [19200]; [57600]; [115200].

Flow Control

Flow control can prevent data loss from buffer overflow. When sending data, if the receiving buffers are full, a “stop” signal can be sent to stop the data flow.

Once the buffers are empty, a “start” signal can be sent to re-start the flow. Hardware flow control uses two wires to send start/stop signals.

The optional settings: [None]; [Hardware RTS/CTS]; [Software Xon/Xoff].

Data Bits

The default setting is: [8].

**This item may or may not show up, depending on different configuration.*

Parity

The default setting is: [None].

**This item may or may not show up, depending on different configuration.*

Stop Bits

The default setting is: [1].

**This item may or may not show up, depending on different configuration*

▶ **PC Health Status**

Press [Enter] to view current hardware health status

▶ **USB Configuration**

Press [Enter] to make settings for the following sub-items:

USB Configuration

XHCI Hand-off

This is a workaround for Oses without XHCI hand-off support. The XHCI ownership change should be claimed by XHCI driver.

The optional settings are: [Enabled]; [Disabled].

USB Mass Storage Driver Support

The optional settings are: [Disabled]; [Enabled].

USB Hardware Delays and Time-outs:

USB Transfer Time-out

Use this item to set the time-out value for control, bulk, and interrupt transfers.

The optional settings are: [1 sec]; [5 sec]; [10 sec]; [20 sec].

Device Reset Time-out

Use this item to set USB mass storage device start unit command time-out.

The optional settings are: [10 sec]; [20 sec]; [30 sec]; [40 sec].

Device Power-up Delay

Use this item to set maximum time the device will take before it properly reports itself to the host controller.

The optional settings: [Auto]; [Manual].

'Auto' uses default value: for a root port it is 100 ms, for a hub port the delay is taken from hub descriptor.

Select [Manual] you can set value for the following sub-item: '**Device Power-up Delay in Seconds**'.

Device Power-up Delay in Seconds

The delay range is from [1] to [40] seconds, in one second increments.

▶ **Network Stack Configuration**

Press [Enter] to go to '**Network Stack**' screen to make further settings.

Network Stack

Use this item to enable or disable UEFI Network Stack.

The optional settings are: [Enabled]; [Disabled].

When set as [Enabled], the following sub-items shall appear:

Ipv4 PXE Support

The optional settings are: [Disabled]; [Enabled].

Use this item to enable Ipv4 PXE Boot Support. When set as [Disabled], Ipv4 boot option will not be created.

Ipv6 PXE Support

The optional settings are: [Disabled]; [Enabled].

Use this item to enable Ipv6 PXE Boot Support. When set as [Disabled], Ipv6 boot option will not be created.

PXE boot wait time

Use this item to wait time in seconds to press ESC key to about the PXE boot. Use either +/- or numeric Keys to set the value.

Media Detect Count

Use this item to set number of times presence of media will be checked.

The optional setting range is from [1] to [50].

▶ **NVMe Configuration**

Press [Enter] to make settings for the following sub-items:

**Note: options only when NVMe device is available.*

▶ **Wake-up Function Settings**

Press [Enter] to make settings for the following sub-items:

Wake-up System with Fixed Time

Use this item to enable or disable system wake-up on alarm event.

The optional settings: [Disabled]; [Enabled].

When set as [Enabled], system will wake on the hour/min/sec specified.

Wake-up Hour

Use this item to select 0-23. For example enter 3 for 3am and 15 for 3pm.

Wake-up Minute

Use this item to select 0-59.

Wake-up Second

Use this item to select 0-59.

Wake-up System with Dynamic Time

Use this item to enable or disable system wake-up on alarm event.

The optional settings: [Disabled]; [Enabled].

When set as [Enabled], system will wake on the current time + increased minute(s). The settings range is from [1] ~ [60] minute(s).

Wake-up Time Increase

Use this item to 1 to 60 minute(s)

USB Power Gating S4-S5

Use this item to USB Wake-up is affected by ERP function in S4, Please disable ERP before activating this function in S4.

The optional settings: [Disabled]; [Enabled].

PCIe Wake-up from S3-S5

The optional settings: [Enabled]; [Disabled].

▶ **PTT Configuration**

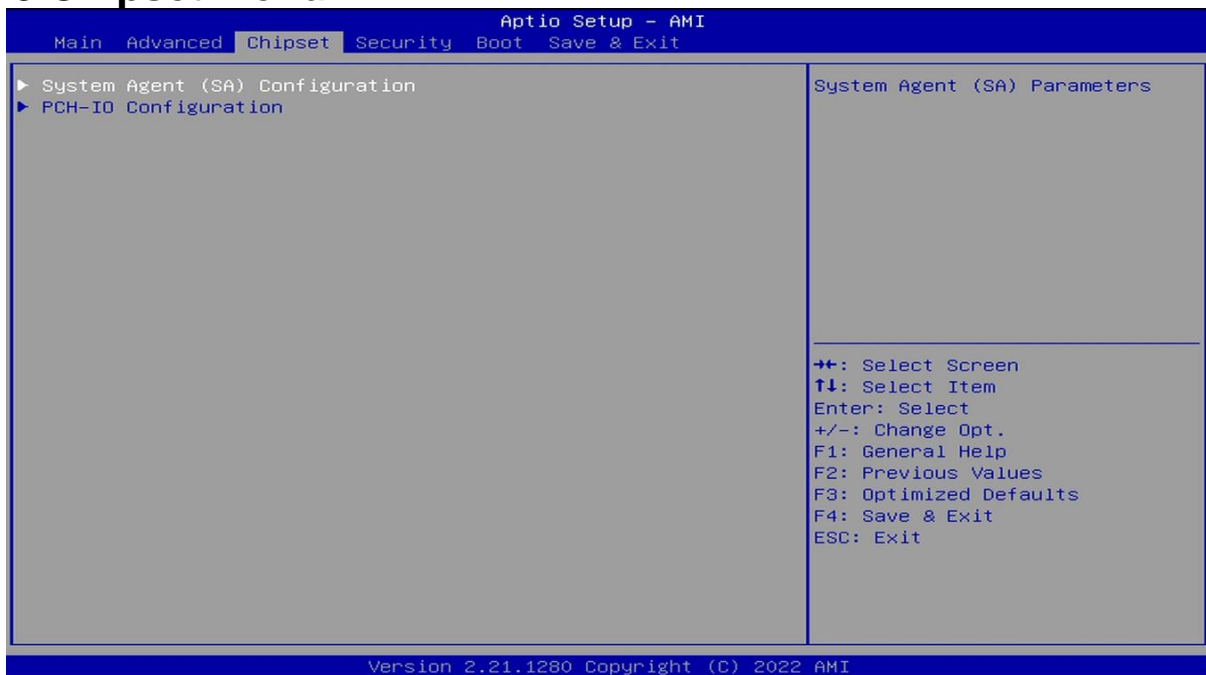
Press [Enter] to make settings for the following sub-items:

TPM Device Selection

Use this item to select TPM device: PTT or dTPM. PTT-Enables PTT IN SkuMgr
dTPM- Disables PTT in SkuMgr.

Warning! PTT/ Dtpm will be disabled and all data saved on it will be lost
The optional settings: [dTPM]; [PTT].

3-8 Chipset Menu



▶ System Agent (SA) Configuration

Press [Enter] to make settings for the following sub-items:

System Agent (SA) Configuration

GTT Size

Use this item to select the GTT Size.

The optional settings: [2MB]; [4MB]; [8MB].

DVMT Pre-Allocated

Use this item to select DVMT 5.0 Pre-Allocated (Fixed) Graphics Memory size used by the Internal Graphics Device.

The optional settings: [0M]; [4M]; [8M]; [12M]; [16M]; [20M]; [24M]; [28M]; [32M]; [36M]; [40M]; [44M]; [48M]; [52M]; [56M]; [60M]; [64M]; [96M]; [128M]; [160M]

DVMT Total Gfx Mem

Use this item to select DVMT 5.0 Total Graphic Memory size used by the Internal Graphics Device.

The optional settings: [128M]; [256M]; [MAX].

Backlight Control

Use this item to back light control setting

The optional settings: [PWM Inverted]; [PWM Normal]

Total Memory

▶ **PCH-IO Configuration**

Press [Enter] to make settings for the following sub-items:

PCH-IO Configuration

▶ **PCI Express Configuration**

Peer Memory Write Enable

Use this item to enable or disable peer memory write.

The optional settings: [Disabled]; [Enabled]

▶ **SATA Configuration**

SATA Controller

Use this item to enable or disable SATA device.

The optional settings: [Disabled]; [Enabled].

When set as **[Enabled]**, the following sub-items shall appear:

SATA Mode Selection

This item determines how SATA controller(s) operate.

The optional settings: [AHCI].

M.2

M.2

The optional settings: [Disabled]; [Enabled].

HD-Audio Support

The optional settings: [Disabled]; [Enabled].

SCS eMMC Support

The optional settings: [Disabled]; [Enabled]

System State after Power Failure

Use this item to specify what state to go to when power is re-applied after a power failure

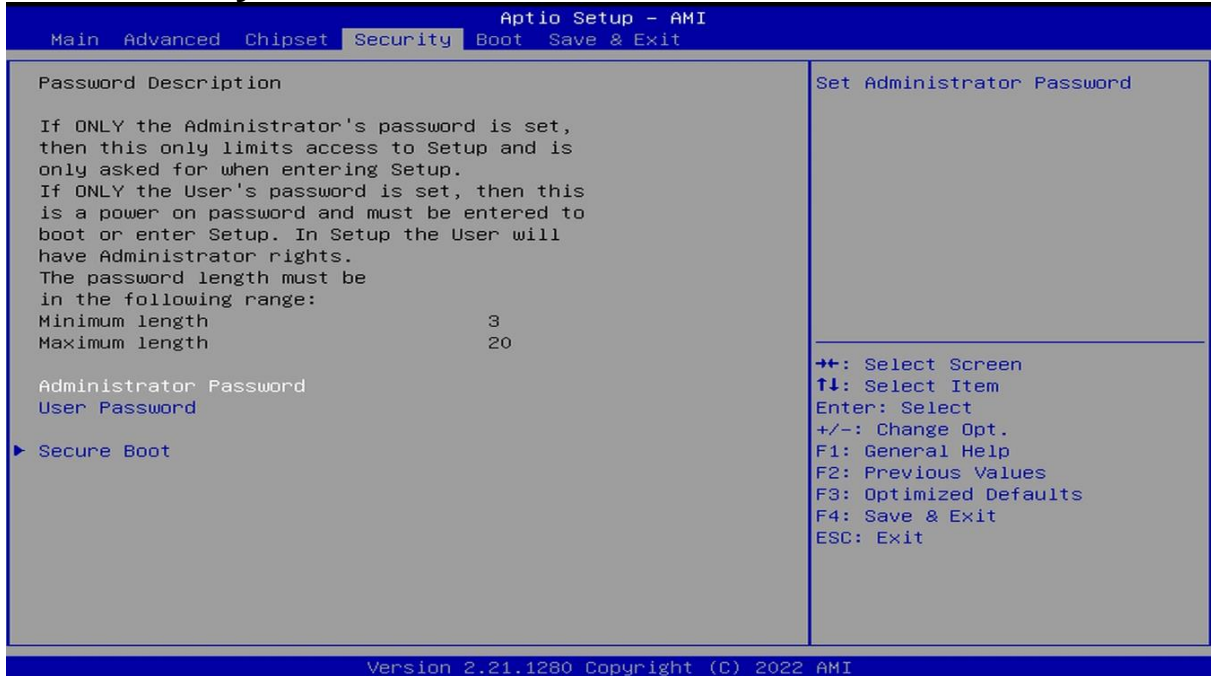
The optional settings: [Always On]; [Always off]; [Former State]

PinCntrl Driver GPIO Scheme

Use this item to enable or disable PinCntrl Driver GPIO Scheme

The optional settings: [Disabled]; [Enabled]

3-9 Security Menu



Security menu allow users to change administrator password and user password settings.

Administrator Password

If there is no password present on system, please press [Enter] to create new setup administrator password. If password is present on system, please press [Enter] to verify old password then to clear/change password. Press again to confirm the new administrator password.

User Password

If there is no password present on system, please press [Enter] to create new user password. If password is present on system, please press [Enter] to verify old password then to clear/change password. Press again to confirm the new user

password.

▶ **Secure Boot**

Press [Enter] to make customized secure settings:

Secure Boot

Secure Boot feature is active if Secure Boot is enabled, Platform Key (PK) is enrolled and the system is in User mode. The mode change requires platform reset.

The optional settings are: [Disabled]; [Enabled].

Secure Boot Mode

Set UEFI Secure Boot Mode to Standard mode or Custom mode. This change is effective after save. After reset, this mode will return to Standard mode.

In Custom mode, Secure Boot Policy variables can be configured by a physically present user without full authentication.

The optional settings are: [Standard]; [Custom].

When set as **[Custom], user can make further settings in the following items that show up:*

▶ **Restore Factory Keys**

Use this item to force system to User Mode, to install factory default Secure Boot key databases.

▶ **Reset To Setup Mode**

Use this item to delete all Secure Boot Key databases from NVRAM.

▶ **Key Management**

This item enables expert users to modify Secure Boot Policy variables without full authentication. Press [Enter] to make settings for the following sub-items:

Factory Key Provision

This item is for user to install factory default secure boot keys after the platform reset and while the system is in Setup mode.

The optional settings are: [Disabled]; [Enabled].

▶ **Restore Factory Keys**

Use this item to force system into User Mode. Install factory default Secure Boot

Key databases.

▶ **Reset to Setup Mode**

Use this item to delete all Secure Boot key databases from NVRAM.

▶ **Export Secure Boot variables**

Use this item to copy NVRAM content of Secure Boot variables to files in a root folder on a file system device.

▶ **Enroll Efi Image**

This item allows the image to run in Secure Boot Mode.

Enroll SHA256 Hash certificate of a PE image into Authorized Signature Database (db).

Device Guard Ready

▶ **Remove 'UEFI CA' from DB**

Device Guard ready system must not list 'Microsoft EFI CA' Certificate in Authorized Signature database (db).

▶ **Restore DB defaults**

Use this item to restore DB variable to factory defaults.

Secure Boot Variable/Size/Keys/Key Source

▶ **Platform Key (PK)/Key Exchange Keys/Authorized Signature/Forbidden Signature/ Authorized TimeStamps/OS Recovery Signatures**

Use this item to enroll Factory Defaults or load the keys from a file with:

1. Public Key Certificate in:

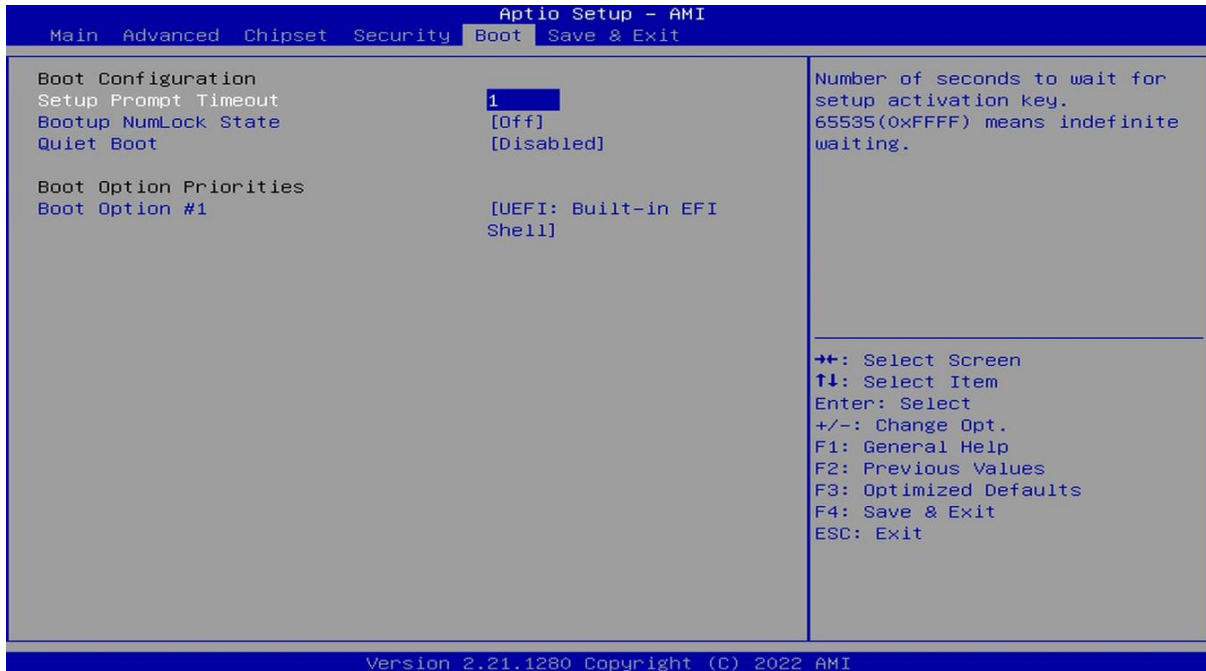
- a) EFI_SIGNATURE_LIST
- b) EFI_CERT_X509 (DER)
- c) EFI_CERT_RSA2048 (bin)
- d) EFI_CERT_SHAXXX

2. Authenticated UEFI Variable

3. EFI PE/COFF Image (SHA256)

Key Source: Factory, External, Mixed.

3-10 Boot Menu



Setup Prompt Timeout

Use this item to set number of seconds to wait for setup activation key.

Bootup NumLock State

Use this item to select keyboard numlock state.

The optional settings are: [On]; [Off].

Quiet Boot

The optional settings are: [Disabled]; [Enabled].

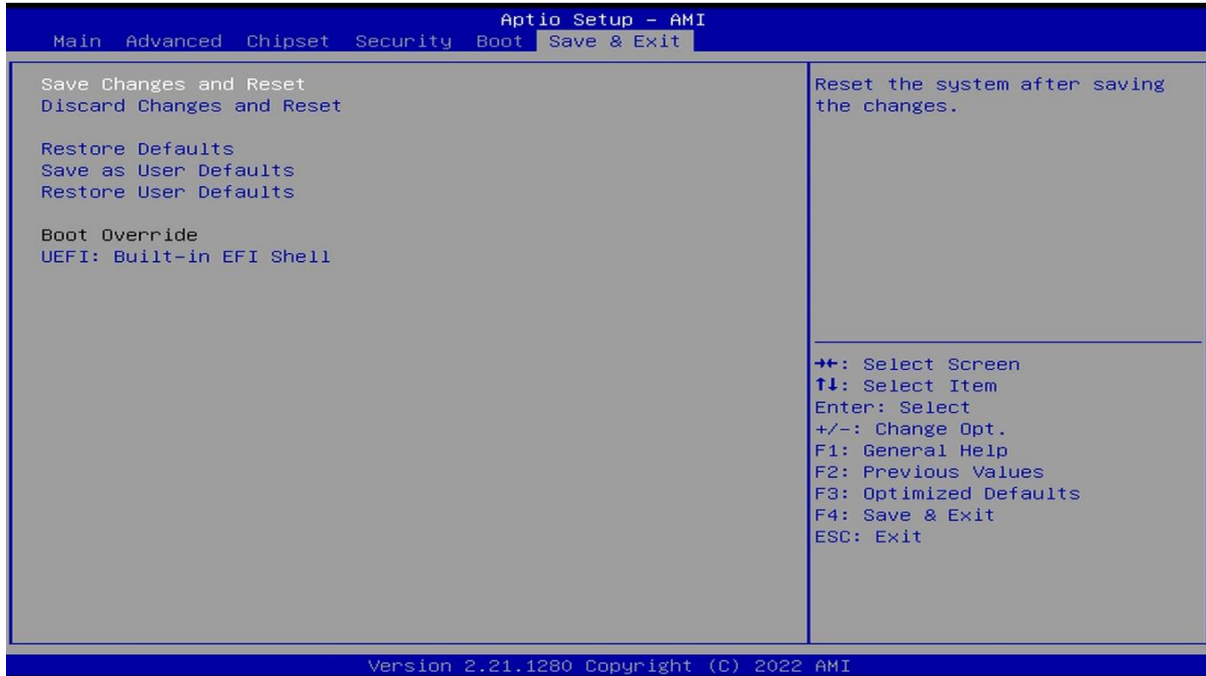
Boot Option Priorities

Boot Option #1

Use this item to sets the system boot order

The optional settings are: [UEFI: Built-in EFI Shell]; [Disabled].

3-11 Save & Exit Menu



Save Changes and Reset

This item allows user to reset the system after saving the changes.

Discard Changes and Reset

This item allows user to reset the system without saving any changes.

Restore Defaults

Use this item to restore /load default values for all the setup options.

Save as User Defaults

Use this item to save the changes done so far as user defaults.

Restore User Defaults

Use this item to restore defaults to all the setup options.

Boot Override

The available options here are dynamically updated and make system boot to any boot option selected.

UEFI: Built-in EFI Shell

Press this item to select the device as boot disk after save configuration and reset.